Cool MOSä Power Transistor

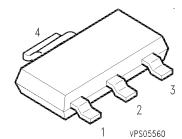
- Worldwide best R_{DS(on)} in SOT 223
- N-Channel
- Enhancement mode
- Ultra low gate charge
- Avalanche rated
- dv/dt rated
- 150°C operating temperature

1	2, 4	3
G	D	S

Туре	V _{DS}	I _D	R _{DS(on)}	Marking	Package	Ordering Code
SPNX6N60S5	600 V	0.85 A	950 m Ω	X6N60S5	P-SOT223-4-1	-
					-	

Maximum Ratings, at $T_i = 25$ °C, unless otherwise specified

Parameter	Symbol	Value	Unit
Drain source voltage	V _{DSS}	600	V
Continuous drain current ¹⁾	I _D		Α
<i>T</i> _C = 25 °C		0.85	
$T_{\rm C} = 100 ^{\circ}{\rm C}$		0.5	
Pulsed drain current	I _{D puls}	tbd	
<i>T</i> _C = 25 °C			
Avalanche energy, single pulse	E _{AS}	140	mJ
$I_{\rm D}$ = 0.85 A, $V_{\rm DD}$ = 50 V, $R_{\rm GS}$ = 25 Ω			
Avalanche current (periodic, limited by T_{jmax})	I _{AR}	tbd	A
Avalanche energy (10 kHz, limited by T_{jmax})	E _{AR}	tbd	mJ
Reverse diode d <i>v</i> /d <i>t</i>	d <i>v</i> /dt	6	KV/µs
<i>I</i> _S = 0.85 A, <i>V</i> _{DS} < <i>V</i> _{DSS} , d <i>i</i> /d <i>t</i> = 100 A/μs,			
T _{jmax} = 150 °C			
Gate source voltage	V _{GS}	±20	V
Power dissipation, $T_{\rm C}$ = 25 °C	P _{tot}	1.8	W
Operating temperature	Tj	-55+150	°C
Storage temperature	T _{stg}	-55 +150	
IEC climatic category; DIN IEC 68-1		55/150/56	



Electrical Characteristics

Parameter	Symbol	Values			Unit	
at T_{i} = 25 °C, unless otherwise specified		min.	typ.	max.		
Thermal Characteristics						
Thermal resistance, junction - soldering point (Pin 4)	R _{thJS}	-	-	tbd	K/W	
Thermal resistance, junction - ambient	R _{thJA}	-	-	-		
(Leaded and through-hole packages)						
SMD version, device on PCB:	R _{thJA}					
@ min. footprint		-	tbd	-		
@ 6 cm ² cooling area ¹⁾		-	70	-		

Static Characteristics

Drain- source breakdown voltage	V _{(BR)DSS}	600	-	-	V
$V_{\rm GS}$ = 0 V, $I_{\rm D}$ = 0.25 mA					
Gate threshold voltage, $V_{GS} = V_{DS}$	V _{GS(th)}				
<i>I</i> _D = 200 μA, <i>T</i> _j = 25 °C		3.5	4.5	5.5	
<i>I</i> _D = 200 μA, <i>T</i> _j = 150 °C		tbd	-	-	
Zero gate voltage drain current, $V_{DS} = V_{DSS}$	I _{DSS}				μA
$V_{\rm GS} = 0 \text{V}, T_{\rm j} = -40 ^{\circ}\text{C}$		-	-	0.1	
$V_{GS} = 0 \text{ V}, T_{j} = 25 \text{ °C}$		-	0.5	1	
$V_{\rm GS} = 0 \text{V}, T_{\rm j} = 150 ^{\circ} \text{C}$		-	-	tbd	
Gate-source leakage current	I _{GSS}	-	10	100	nA
$V_{\rm GS} = 20 \text{ V}, \ V_{\rm DS} = 0 \text{ V}$					
Drain-Source on-state resistance	R _{DS(on)}	-	tbd	950	mΩ
$V_{\rm GS}$ = 10 V, $I_{\rm D}$ = 0.5 A					

¹ Device on 50mm*50mm*1.5mm epoxy PCB FR4 with 6 cm2 (one layer, 70µm thick) copper area for drain connection. PCB is vertical without blown air.

Electrical Characteristics

Parameter	Symbol		Unit		
at $T_j = 25 \text{ °C}$, unless otherwise specified		min.	typ. max.		
Characteristics		•			
Transconductance	<i>9</i> fs	-	tbd	-	S
$V_{\text{DS}} \ge 2 * I_{\text{D}} * R_{\text{DS(on)max}}$, $I_{\text{D}} = 0.5 \text{ A}$					
Input capacitance	C _{iss}	-	580	tbd	pF
$V_{\rm GS} = 0 \text{ V}, V_{\rm DS} = 25 \text{ V}, f = 1 \text{ MHz}$					
Output capacitance	C _{oss}	-	375	tbd	
$V_{\rm GS} = 0 \text{ V}, V_{\rm DS} = 25 \text{ V}, f = 1 \text{ MHz}$					
Reverse transfer capacitance	C _{rss}	-	20	tbd	
$V_{\rm GS} = 0 \text{ V}, V_{\rm DS} = 25 \text{ V}, f = 1 \text{ MHz}$					
Turn-on delay time	t _{d(on)}	-	tbd	tbd	ns
$V_{\text{DD}} = 350 \text{ V}, V_{\text{GS}} = 10 \text{ V}, I_{\text{D}} = 0.85 \text{ A},$					
R _G = 18 Ω					
Rise time	<i>t</i> r	-	tbd	-	
$V_{\text{DD}} = 350 \text{ V}, V_{\text{GS}} = 10 \text{ V}, I_{\text{D}} = 0.85 \text{ A},$					
$R_{\rm G}$ = 18 Ω					
Turn-off delay time	t _{d(off)}	-	tbd	tbd	
$V_{\text{DD}} = 350 \text{ V}, V_{\text{GS}} = 10 \text{ V}, I_{\text{D}} = 0.85 \text{ A},$					
R _G = 18 Ω					
Fall time	t _f	-	tbd	-	
$V_{\text{DD}} = 350 \text{ V}, \ V_{\text{GS}} = 10 \text{ V}, \ I_{\text{D}} = 0.85 \text{ A},$					
<i>R</i> _G = 18 Ω					

Electrical Characteristics

Parameter	Symbol		Unit		
at T_{i} = 25 °C, unless otherwise specified		min.	typ.	max.	
Gate Charge Characteristics	·	•	•	•	
Gate-source charge	Qgs	-	tbd	-	nC
$I_{\rm D} = 0.85 \text{ A}, V_{\rm DD} = 400 \text{ V}$					
Gate-drain charge	Q _{gd}	-	tbd	-	
$I_{\rm D} = 0.85 \text{ A}, V_{\rm DD} = 400 \text{ V}$					
Total gate charge	Q _G	-	19	tbd	
V_{DD} = 400 V, I_{D} = 0.85 A, V_{GS} = 0 to 10 V					
Reverse DiodeContinuous source current $T_{\rm C} = 25 \ ^{\circ}{\rm C}$	I _S	-	-	0.85	A
Pulsed source current $T_{\rm C}$ = 25 °C	/ _{SM}	-	-	tbd	
Inverse diode forward voltage $V_{GS} = 0 \text{ V}, I_{F} = 0.85 \text{ A}$	V _{SD}	-	tbd	1.2	V
Reverse recovery time $V_{\rm R} = 100 \text{ V}, I_{\rm F}=I_{\rm S}, di_{\rm F}/dt = 100 \text{ A}/\mu \text{s}$	t _{rr}	-	tbd	-	ns
Reverse recovery charge	Q _{rr}	-	tbd	-	μC

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